

Implementation of Digital Pic on Troller for Dc To Dc Boost Converter Connected To Ev

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ABSTRACT: The recent upsurge in the demand of PV systems is due to the fact that they produce electric power without hampering the environment by directly converting the solar radiation into electric power. However the solar radiation never remains constant. It keeps on varying throughout the day. The need of the hour is to deliver a constant voltage to the grid irrespective of the variation in temperatures and solar insolation. We have designed a circuit such that it delivers constant and stepped up dc voltage to the load. We have studied the open loop characteristics of the PV array with variation in temperature and irradiation levels. Then we coupled the PV array with the boost converter in such a way that with variation in load, the varying input current and voltage to the converter follows the open circuit characteristic of the PV array closely. At various insolation levels, the load is varied and the corresponding variation in the input voltage and current to the boost converter is noted. It is noted that the changing input voltage and current follows the open circuit characteristics of the PV array closely.

I. INTRODUCTION:

In the last few decades, there has been tremendous increase in the use of switch mode power supplies (SMPS) due to their suitability in the applications of renewable energy, automobile etc. The basic power electronic DC-DC converter are buck, boost and buck-boost. DC-DC boost converter finds enormous applications in industries, few of them are in hybrid electric vehicle, thermoelectric generator

power conditioning systems, power amplifiers, adaptive control applications. In each of these cases, the input outside voltage or the load side may undergo sudden variations. For a hybrid electric vehicle the battery voltage may change at the input, or in applications such as in power conditioning system the boost converter may be connected in series with the rectifier circuit which may have variation in the input voltage, i.e. the second harmonics at the source voltage.

Similarly in case of thermos electric generator also the input voltage is a function of heat energy coming from the thermal system. Hence boost type DC-DC converter seems to have numerous applications, but in most of the industrial applications, these power electronic converters are subjected to external disturbances such as line voltage and load variations while at the same time need to achieve desired constant voltage at the output. Thus there is a need to apply proper control strategies which will regulate the output voltage of boost converter.

To attain this objective in these switching power converters, a suitable controller is required and in conventional approaches, linear PID controller has been used in many applications. Using the small-signal models of the DC-DC converters, derived from state space model, linear PI controllers are in general designed through classical frequency response techniques such as bode plots, in which the desired loop gain, crossover frequency, and phase margin can be adjusted to guarantee the stability of the overall system.

OBJECTIVE

Project:

- Modelling and design aspects of DC to DC Boost converter for EV application.
- To develop and analyze the closed loop PI control technique using MATLAB.
- To implement the digital PI Controller and assess the results.

This project is beneficial to power monitoring and display ed data on server to identify how manpower is used. In this circuit, in the night the lamp will automatically ON by using LD RC circuit, N need to operates lamp manually.

Voltage and current can be easily measured with the help of current and voltage sensor, no need voltmeter to measure these quantities and also power can be measured.

Needs of Digital PI Controller:

- It needs to use at least one DC/DC converter to interface battery and DC appliance.
- In irregular voltage direction DC/DC converter can be designed to transfer power in one direction from input to output.
- DC/DC converter is an electric circuit that converts source of DC from one voltage level to another, and releasing a required voltage.

MOTIVATION:

The conventional sources of energy are rapidly depleting. Moreover the cost of energy is rising and therefore photovoltaic system is a promising alternative. They are abundant, pollution free, distributed throughout the earth and recyclable. The hindrance factor is its high installation cost and low conversion efficiency.

Therefore our aim is to increase the efficiency and power output of the system. It is also required that constant voltage be supplied to the load irrespective

of the variation in solar irradiance and temperature. PV arrays consist of parallel and series combination of PV cells that are used to generate electrical power depending upon the atmospheric conditions (e.g. solar irradiation and temperature). So it is necessary to couple the PV array with a boost converter. Moreover our system is designed in such a way that with variation in load, the change in input voltage and power fed into the converter follows the open circuit characteristics of the PV array. Our system can be used to supply constant stepped up voltage to dc loads

II. PROPOSED SYSTEM:

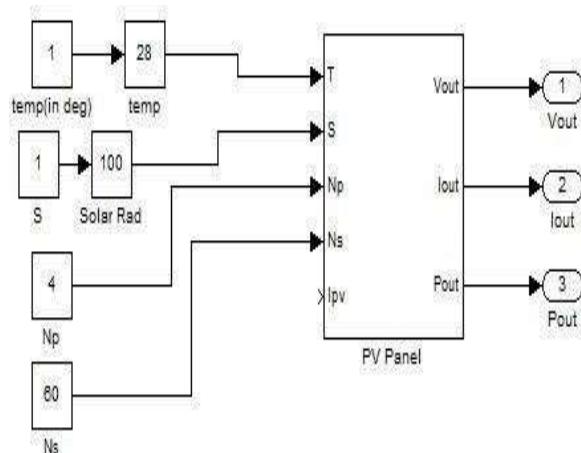


Fig.1 block diagram of PV Panel

III. SYSTEM PERFORMANCE



MATERIALS USED IN PV CELL:

The materials used in PV cells are as follows:

➤ Single-crystal silicon

Single-crystal silicon cells are the most common in the PV industry. The main technique for producing single-crystal silicon is the Czochralski (CZ) method. High-purity polycrystalline

is melted in a quartz crucible. A single-crystal silicon seed is dipped into this molten mass of polycrystalline. As the seed is pulled slowly from the melt, a single-crystal ingot is formed. The ingots are then sawed into thin wafers about 200-400 micrometers thick (1 micrometer = 1/1,000,000 meter). The thin wafers are then polished, doped, coated, interconnected and assembled into modules and arrays.

➤ Polycrystalline silicon

Consisting of small grains of single-crystal silicon, polycrystalline PV cells are less energy efficient than single-crystalline silicon PV cells. The grain boundaries in polycrystalline silicon hinder the flow of electrons and reduce the power output of the cell. A common approach to produce polycrystalline silicon PV cells is to slice thin wafers from blocks of cast polycrystalline silicon. Another more advanced approach is the "ribbon growth" method in which silicon is grown directly as thin ribbons or sheets with the approach thickness for making PV cells.

➤ Gallium Arsenide (GaAs)

A compound semiconductor made of two elements: Gallium (Ga) and Arsenic (As). GaAs has a crystal structure similar to that of silicon. An advantage of GaAs is that it has a high level of light absorptivity. To absorb the same amount of sunlight, GaAs

requires only a layer of few micrometers thick while crystalline silicon requires a wafer of about 200-300 micrometers thick. Also, GaAs has much higher energy conversion efficiency than crystalline silicon, reaching about 25 to 30%. The only drawback of GaAs PV cells is the high cost of single crystal substrates that GaAs is grown on.

➤ Cadmium Telluride (CdTe)

It is a polycrystalline compound made of cadmium and telluride with a high absorptivity capacity (i.e. a small thin layer of the compound can absorb 90% of solar irradiation). The main disadvantage of this by-product is the presence of cadmium telluride.

➤ Copper Indium Diselenide (CuInSe₂)

It is a polycrystalline compound semiconductor made of copper, indium and selenium. It delivers high energy conversion efficiency without suffering from outdoor degradation problem. It is a compound that has the instability of PV cell or module performance. As it is a toxic substance, the manufacturing process should be done. One of the most light-absorbent semiconductors. As it is a complex material and toxic in nature, so the manufacturing process faces some problem.

DESIGN OF THE BOOST CONVERTER:

(1) CURRENT RIPPLE FACTOR (CRF):

According to IEC harmonics standard, CRF should be bounded within 30%.

$$\frac{\Delta I}{I_0} = 30\% \quad (4.5)$$

1

(2) VOLTAGE RIPPLE FACTOR (VRF):

$$\frac{\Delta V}{V_0} = 5\% \quad (4.6)$$

0

(3) SWITCHING FREQUENCY (f_s):

$$f_s = 100 \text{ KHz} \quad (4.7)$$

GIVEN DATA:

- Input voltage, $V_g = 25 \text{ V}$
- Output voltage, $V_o = 300 \text{ V}$
- Output load current, $I_o = 1 \text{ A}$

Step 1: Calculation of Duty cycle (D):

$$D = \frac{1}{1 - \frac{V_o}{V_g}}$$

$$D = \frac{1}{1 - \frac{300}{25}} = \frac{300}{25}$$

$$\square D = 11/12 = .9166 \quad (4.8)$$

Step 2: Calculation of Ripple Current (Δ):

$$I_L = 1A$$

$$\square \Delta = (0.3 * 1) A = 0.3 A \quad (4.9)$$

Step 3: Calculation of Inductor value (L):

$$L = \frac{V_g}{f * \Delta} = \frac{25 * .9166}{(0.3 * 10^5)} = 7.63 * 10^{-4} H \quad (4.10)$$

Step 4: Calculation of capacitive value (C):

$$\text{We have, } \frac{\Delta V}{0} = \frac{R_o}{0} \quad (4.11)$$

$$R_o = \frac{0}{300/1} = 300 \Omega \quad (4.12)$$

$$C = D/f * R_o * (\Delta V_0/V) = (.9166)/(10^5) * (300) * (.05) = .611 \mu F \quad (4.13)$$

The transfer function of the boost converter used for the modeling is given by:

$$G(s) = \frac{1 - \frac{1}{sC}}{1 - \frac{1}{sL} + \frac{1}{s^2L^2}} \quad (4.14)$$

Putting the values of R, L, C, D, V_g in the above equation the transfer equation that results is given by:

$$G(s) = \frac{25 * (300 - (100.716 * 10^{-3}))((.139 * 10^{-6}) * s^2 + 0.763 * (10^{-3}) * s + 2)}{0.08} \quad (4.15)$$

By trial and error, we get the value of K_p which gives desired results as.

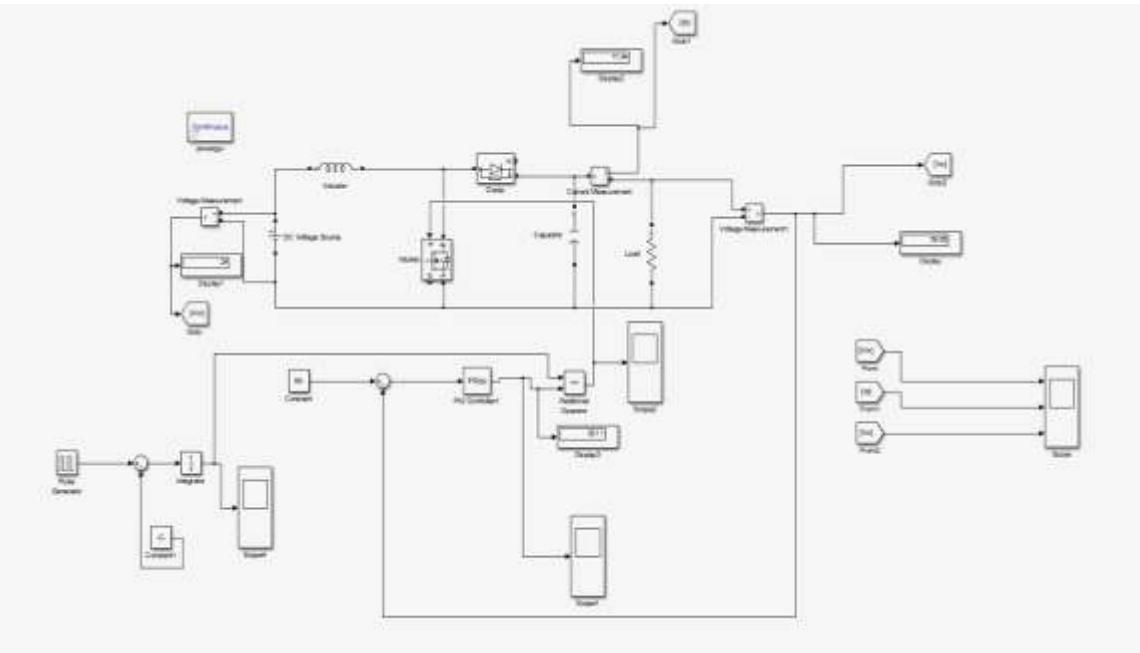
IV. SOFTWARE DETAILS:

SIMULINK MODEL:

The Figure below shows the block diagram of the complete circuit. This includes the

PV module, boost converter and control circuit. The modeling and simulation of the whole system has been done in MATLAB-SIMULINK environment.

Figure.The complete simulation model of the PV energy conversion system.



GENERATION OF THE PWM SIGNAL:

The simulink model for the generation of the PWM signal is shown below:

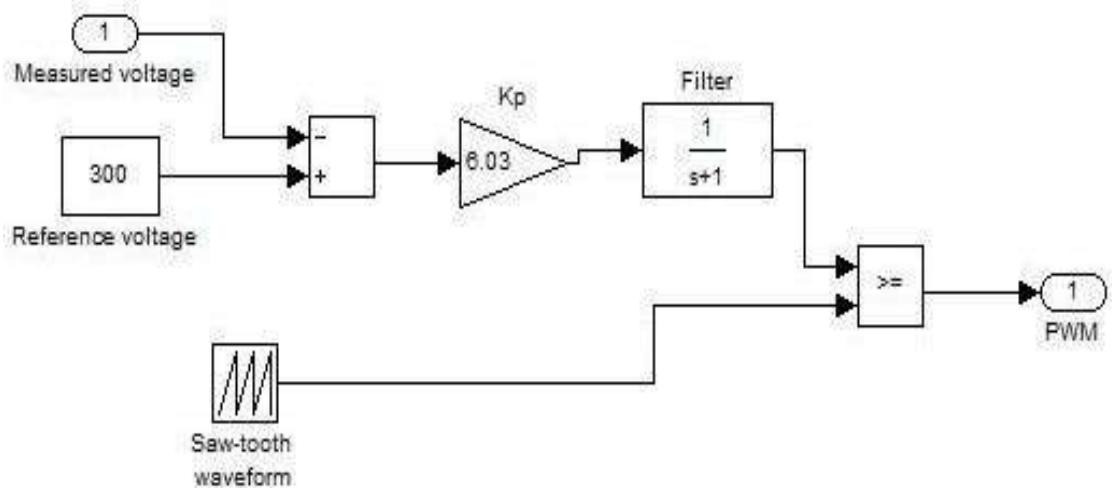


Figure 5.4.1 Circuit diagram for PWM signal generation

The output of the filter which is the control signal is compared with the saw-tooth waveform having a peak voltage of 25V to generate the PWM signal which is fed as gate signal to

the MOSFET. The simulation diagram showing the comparison of the saw-tooth waveform with the control signal is shown below:

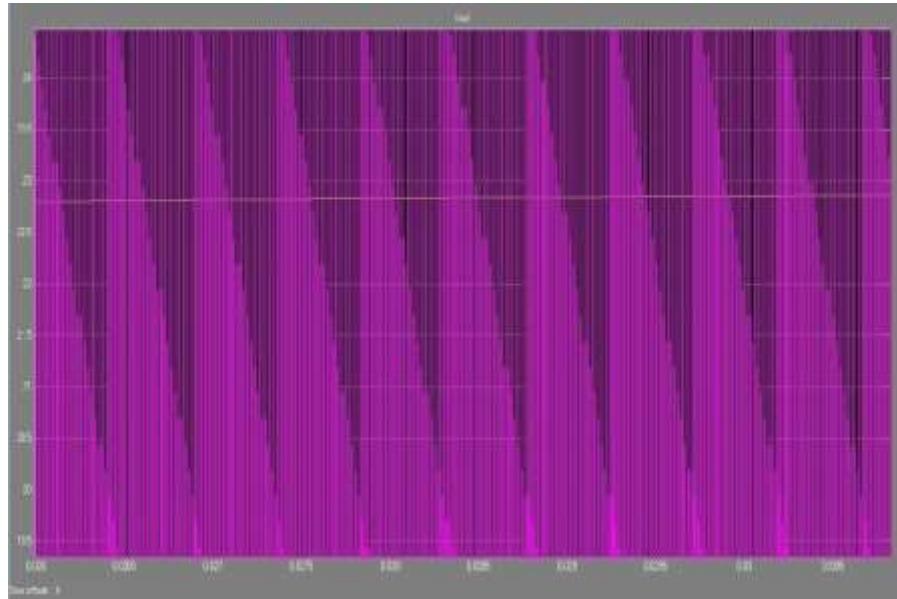


Figure 5.4.2 Simulation diagram showing the generation of the PWM signal

The required PWM signal used as the gate pulse for the MOSFET is shown below in the figure:

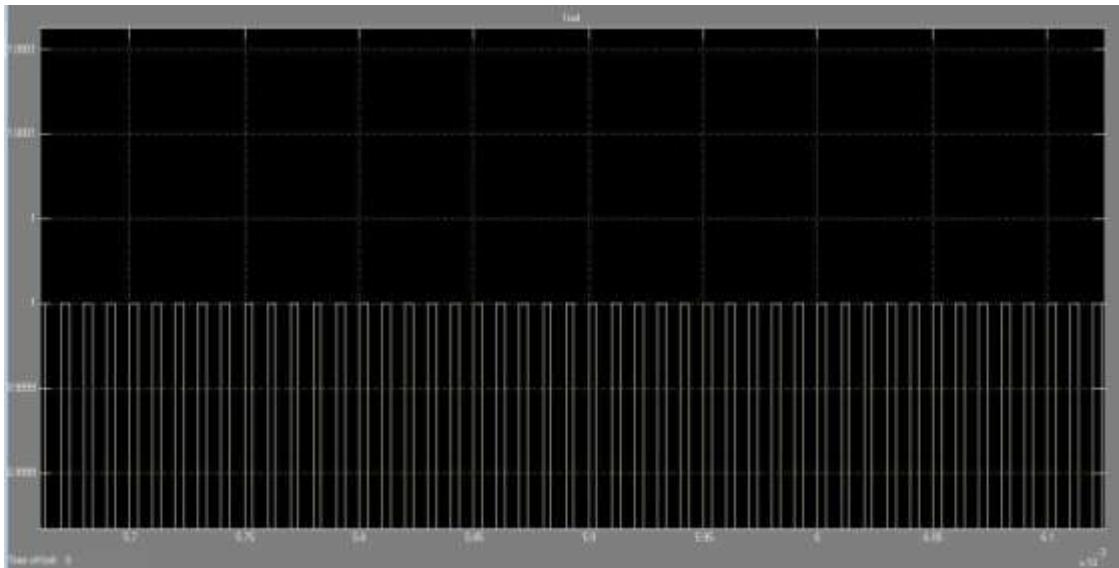
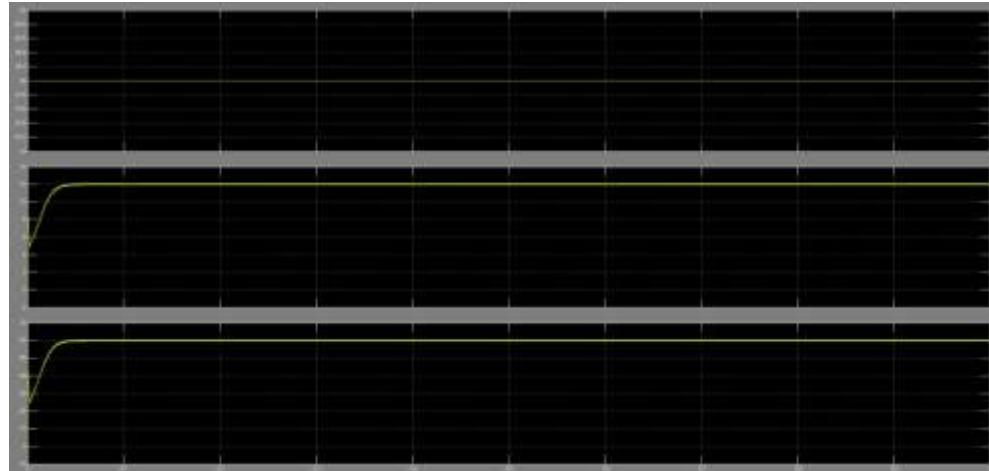


Figure 5.4.3 PWM signal generated

Result Analysis:

The output I_{out} and V_{out} curves obtained across the load resistance of the boost converter of the simulink model as shown in above Figure(5.5) is drawn below.

Figure 5.5 The voltage output and current across the load resistance of the boost converter



RESULTS CONFIRMING PROPER COUPLING OF PV ARRAY WITH BOOST CONVERTER:

The load resistance of the close-loop boost converter is varied and the values of the input voltage and current fed to the converter are noted for various levels of isolation. The values of the

current and voltage obtained are plotted in the open circuit I-V curve of the PVarray. The values obtained follow the curve closely thereby fulfilling our requirements. The tabulation and the curves which verify our successful simulation model is given below:

Table 2: Value of input voltage and current for variation in load resistance for an irradiance Level (100 mW/m²)

INSOLATION(mW/m ²)	LOAD RESISTANCE(Ω)	INPUT VOLTAGE(Volt)	INPUT CURRENT(Amp)
100	300	28.92	11.42
100	285	27.2	12.86
100	450	31.62	6.898

Table3:Valueofinputvoltageandcurrentforvariationinloadresistance foranirradiancelevel(80mW/m^2)

INSOLATION(mW/m^2)	LOAD RESISTANCE(Ω)	INPUTVOLTAGE(Volt)	INPUTCURRENT(Amp)
80	400	28.75	8.58
80	450	29.75	7.382
80	680	31.26	4.726

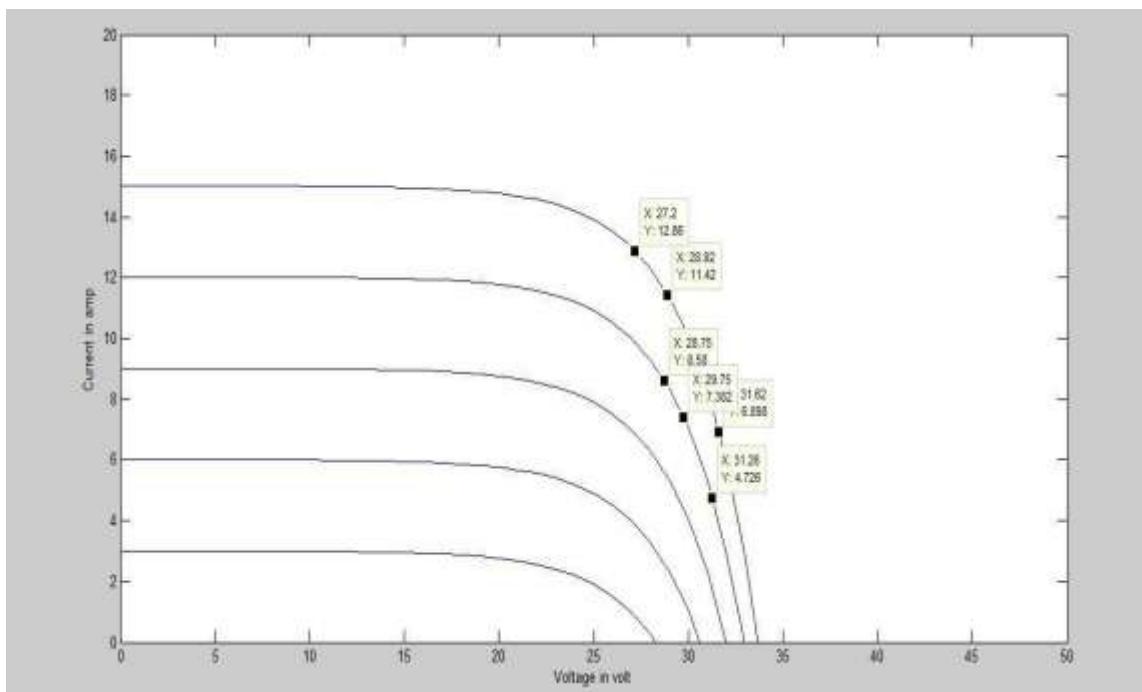


Figure5.6InterfacingofPVarraysimulationresultwithopenloopI-Vcharacteristic

V. CONCLUSION:-

We can operate night lamps automatically by using LDR circuit. Lamps will be glow only when darkness is present. It's economic one, and electricity will be saved. In the present system, energy load consumption is accessed using Wi-Fi and it will help consumers to avoid unwanted use of electricity. We can make a system which can send SMS to the concerned meter reading man of that area when theft is detected at consumer end. Also using cloud analytics we can predict future energy consumptions.

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