



Akash Deo, Deepanshu Sharma, Dipesh Kumar Meena, Devanshu Gupta, Dhananjay , Dharmesh Choudhary

Published following article

Application of Harmonic Mitigation Technique to a VSI fed by 12 pulse fully controlled rectifier circuit using passive filtering

Volume 3, Issue 8, pp: 455-461

www.ijaem.net A Peer Reviewed Journal

International journal of Advances in Engineering and Management (IJAEM)



ISSN: 2395-5252

Publication Head