

# Simulating Neuromorphic Behavior in Memory Devices with Special Ions: Insights into Device Performance and Predictive Modeling.

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ABSTRACT: The development of neuromorphic memory, which mimics the computational processes of the brain, offers exciting prospects for applications in advanced computing. This studyassesses the potential of metal-ion-containing polyvinyl acetate (PVAc) fiber as neuromorphic memory devices. To assess the synaptic plasticity and hysteresis characteristics of the manufactured devices, they underwent a thorough electrical characterization process that included currentvoltage (I-V) testing and pulse stress analysis. With long-term depression (LTD) under negative stress (-6V) and long-term potentiation (LTP) under positive stress (+6V), the results showed that PVAc metal-ion devices display dual-direction plasticity. The plasticity zones showed good adaptability in simulating neuronal learning and mechanisms, forgetting extending up to approximately 300 seconds for LTP and 500 seconds for LTD. These results demonstrate that PVAc + metal-ion devices can be used in neuromorphic applications, especially for energyefficient adaptive memory systems in robotics, artificial neural networks, and edge computing. Although there is promise for neuromorphic applications with these devices, further work is required to overcome their sensitivity to external factors. This work opens the door for future neuromorphic systems and provides important insights into device performance.

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**Keywords:**Neuromorphic; Synaptic; plasticity; Hysteresis; long-term depression (LTD); long-term potentiation (LTP)

### I. INTRODUCTION

Although ionic interactions-based memory devices have attracted considerable interest in recent years due to their neuromorphic behavior concept. In contrast, owing to their elastomeric nature, aqueous electrolytes provide a route to develop intronic resistive memory devices that can emulate their brain-inspired systems with stretches of ionic transfer. This allows for not only the improvement of device performance but also the exploration of different electrode and active layer materials, as well as device architectures (Südhof, 2021).To create dependable brain-inspired resistive memories with consistent performance and repeatability, smooth neurotransmission is made possible by the special characteristics of aqueous intronic resistive memories. Furthermore, it has been suggested that neuromorphic computing frameworks incorporate metal-oxide-based electrochemical random-access memory (ECRAM) devices to accomplish effective parallel processing capabilities. Because of their small cell sizes and low programming energy, ECRAM devicesespecially those with a 3D vertical structure-are appropriate for high-density cross-point arrays(Anik Kumar, Padovani, Larcher, Raiyan Chowdhury, & Zunaid Baten, 2022). Information can be used to determine the update profile of the weight via the manipulation of a train of voltage pulses, demonstrating the delinking of the dynamic range vs update deviation parameter space to provide prediction-based modeling to optimize device traits. There are still challenges to overcome in terms of parameter variation and cycling endurance in the context of memristive devices. These recent advances in the understanding of the physical origins of these non-idealities have shown the necessity of in situ characterization and device modeling to analyze the effects occurring during the actual switching process(Kim, Song, Hwang, Hwang, & Kim, 2024). This comprehensive essential for approach enhancing is the



performance and reliability of memristive technologies, paving the way for their commercialization. Furthermore, the multiscale nature of ischemic stroke provides insights into the dynamic interactions of ions within neural environments. To capture the cascade effects of ischemia, Laham and Gould (2022) stress the significance of linking intracellular chemical changes with electrophysiological responses. Researchers can replicate the intricate behaviors seen in neural tissues and use this information to inform the design of neuromorphic devices by simulating the reaction diffusion of ions in the extracellular space in conjunction with intricate biophysical neuron models. In conclusion, the advancement of neuromorphic computing depends on the incorporation of ionic insights into memory device development. By utilizing the special qualities of aqueous electrolytes, refining ECRAM architectures. tackling memristive technology issues, and applying multiscale modeling techniques, researchers can greatly improve device performance and predictive capabilities in simulating neuromorphic behaviour(Dittmann, Menzel, & Waser, 2021).

### II. MATERIALS AND METHODS

**Equipment and Materials:** Glass substrates, spin coating machine, thermal evaporator. Pico-ammeter and voltage source, Agilent VEE software,

**Methodology:** A rigorous experimental approach was designed to validate the potential of PVAc and metal ion devices to mimic neuromorphic memory. The methodology covered device fabrication, electrical characterization, and data analysis, ensuring a comprehensive evaluation of the devices' neuromorphic properties.

1. Device Fabrication: Two types of devices were intended to be produced by the fabrication process: test devices (PVAc fibers containing metal ions) and reference devices (PVAc fibers without metal ions).

1.1 Preparing the Substrate: Because of their stability and suitability for aluminum deposition, glass substrates were chosen. To guarantee an uncontaminated surface, nitrogen gas was used to clean the substrates of minute contaminants. The deposition was done using thermal evaporation in a vacuum, and a 200 nm thin layer of aluminum (AI) was applied to the substrates. For the electrical characterization of the devices, accurate 100 $\mu$ m gap cells in the AI layer were created using a shadow mask.

1.2 Materials for Preparing the PVAc Solution:One polymer with a reputation for having electro strictive qualities is polyvinyl acetate (PVAc) and methanol was used to dissolve PVAc uniformly.To improve ionic mobility and mimic synaptic activity, metal ions were introduced.To guarantee full dissolving, 300 mg of PVAc was dissolved in 3 ml of methanol using an ultrasonic bath.To facilitate ionic transport, 1.5 mg of metal ions were added to the PVAc-methanol solution using the same procedure.

1.3 Production of Fibers: To make PVAc fibers, the spin-coating method was used: the spin-coating for reference devices, the spin speed is 9,000 RPM; for test devices, it can range from 5,000 to 10,000 RPM to evaluate fiber consistency. Accurate fiber extrusion was guaranteed by a specially made aluminum spin cup. To guarantee electrical continuity, fibers were meticulously extracted and positioned across the aluminum gap cells using silver epoxy after spin coating.

2. Characterization of Electrical: I-V testing and pulse stress testing were the two forms of characterization used to evaluate the neuromorphic characteristics.

2.1 Characterization of I-V: Under various electrical stimuli, the current-voltage relationships of the devices were assessed, and the voltage range of -5V to +5V was used at 100 cycles to capture dynamic changes.Ten milliseconds pass between measurements. The electron microscope-shieldedprobe station removed electromagnetic interference to guarantee accuracy.A Voltage-Powered Pico Ammeter Source was accurately used to measure the current.

2.2 Stress Testing using Pulses: By exposing devices to constant electrical stress, pulse stress testing mimicked real-world neuromorphic applications. The stress voltages were +6V and -6V, at ten seconds for each cycle of stress. The voltage was applied incrementally for accuracy and each stress condition has 100 cycles.

3. Data Analysis: Data from the electrical characterization was processed to identify patterns and neuromorphic traits.

3.1 Analysis of Hysteresis: To measure the devices' capacity for memory retention, the area of the hysteresis loop was computed.

3.2 Evaluation of Plasticity: To ascertain LTP and LTD behaviour, changes in Imax over cycles were examined. The plasticity region was found by first-order differentiation of Imax curves, which revealed information about operating constraints.



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## III. RESULT AND DISCUSSION 1. Plasticity Assessment:

One of the characteristics of neuromorphic memory devices is plasticity, or the capacity to alter synaptic strength. Under positive and negative stress voltages, the fabricated PVAc + metal-ion devices showed unique long-term potentiation (LTP) and depression (LTD) characteristics.

Long-Term Potentiation (LTP):The devices showed an increase in conductance when exposed to a constant positive stress voltage of +6V. The observed increase aligns with the strengthening of synaptic connections in biological systems(Südhof, 2021). The plasticity zone in Figure 1, displays notable variations in Imax up to 300s before plateauing. The device may be able to simulate synaptic strengthening based on the observed potentiation. This result is consistent with earlier research that found plasticity saturation in memristive devices as a result of charge-trapping effects (Lee, et al., 2021; Anik Kumar, et al., 2022). Because of their LTP behavior, these gadgets could be good choices for real-time learning tasks that call for gradual synaptic weight building, such as image recognition.



Figure 1: Plasticity region for Device 1 under +6V stress, demonstrating long-term potentiation (LTP). Source: Researchers Fieldwork, 2023

Long-Term Depression (LTD): The devices' conductance decreased when exposed to a negative stress voltage of -6V. Figure 2 shows that plasticity persisted until about 500s, suggesting prolonged synaptic weakening in contrast to LTP.This behavior mimics the biological mechanisms of LTD and is indicative of synaptic depression. Kumar et al. (2022) observed similar outcomes in devices based on memristors, highlighting the significance of this dual-direction plasticity in neuromorphic systems.



Figure 2: Plasticity region for Device 1 under -6V stress, showing long-term depression (LTD). Source: Researchers Fieldwork, 2023

#### 2. Hysteresis Behaviour:

The region of I-V loops indicates hysteresis behaviour, which is essential for neuromorphic emulation and memory retention. As seen in Figure 3, the hysteresis loop area grew under positive stress. This illustrates the device's capacity to retain information by showing how its increases with each conductance cvcle.Bv promoting ionic transport, metal ions markedly improved memory recall. These findings are consistent with those ofLa Mendolaet al. (2021), who noted comparable patterns in phase-change memory devices.



Figure 3: Area plot of the pulse stress I-V characteristics curve for Device 1 (+6V stress), showing increasing memory retention. Source: Researchers Fieldwork, 2023

The hysteresis loop area contracted at the Negative Stress (-6V), as shown in Figure 4, indicating a decrease in conductance over cycles. The behaviour suggests the device's capacity for synaptic weakening under negative stimuli. This



characteristic is essential for systems requiring dynamic adaptability.



Figure 4: Area plot of the pulse stress I-V characteristics curve for Device 1 (-6V stress), showing reduced conductance. Source: Researchers Fieldwork, 2023

3. Comparison of Reference and Test Devices:

The inclusion of metal ions had a transformative impact on device performance. Test devices demonstrated robust hysteresis and plasticity, as shown in Figure 5, while reference devices exhibited limited conductance modulation and narrow hysteresis loops. Metal ions played a critical role in enhancing synaptic behaviour by enabling dynamic ionic interactions. Similar enhancements were observed in research carried out by Li et al. (2020), emphasizing the role of ionic components in neuromorphic systems.



Figure 5: Area plot of the pulse stress I-V characteristics curve for the fiber reference device, illustrating limited plasticity. Source: Researchers Fieldwork, 2023

### IV. CONCLUSION

This study reveals that metal-ion memory devices combined with polyvinyl acetate (PVAc) can replicate neuromorphic memory processes.

fabrication, Through device electrical characterization, and data analysis, the studyexhibits important neuromorphic characteristics such as hysteresis, dynamic conductance modulation, and synaptic plasticity (LTP and LTD).Critical neuronal learning and memory mechanisms were replicated by the devices, which demonstrated long-term depression (LTD) under negative stress and long-term potentiation (LTP) under positive stress.For LTP and LTD, the plasticity areas were prolonged to around 300 and 500 seconds, respectively, suggesting modest but effective operational adaptability.In comparison to reference devices, the devices' hysteresis was greatly improved by the addition of metal ions, enabling bigger loop regions and improved memory retention. The dynamic transit of ions inside the PVAc matrix, which replicates the biological function of ion channels in brain synapses, is responsible for this enhancement. The study's overall findings support the viability of employing PVAc + metal-ion devices in neuromorphic applications, especially in lowpower, adaptive systems such as artificial neural networks, robotics, and edge computing. The device's capacity to mimic important synaptic functions is a major advancement in neuromorphic computing, even though they are still unable to fully imitate the complexity and efficiency of biological neurons. To fully utilize these devices in bridging the gap between artificial systems and biological intelligence, future research concentrating on scalability, material stability, and application-specific testing will be crucial.

### V. RECOMMENDATION

The development of neuromorphic memory devices using metal-ion-containing PVAc proposed several recommendations. fibers Extending plasticity zones and improving device behaviour should be the top priorities for predictive modeling using machine learning and thorough charge-trapping studies. To ensure scalability and consistency, design efforts should concentrate on application-specific requirements, customizing devices for robotics, artificial neural networks, and edge computing. Achieving low-voltage operation and implementing power-optimization circuits can efficiency. Ionic transport increase energy stabilization and durability testing are necessary for long-term stability to reduce performance degradation.



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